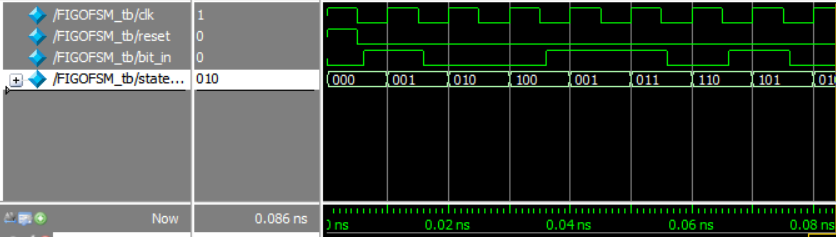
**Output waveform on Running Testbench:**

****

**Pin Planning:**

A table with text and numbers

Description automatically generated

**Switch allocation:**

Switch 0: bit\_in

Switch 1: clk

Switch 2: reset

**Output LEDs:** LED[2], LED[1], LED[0]